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# PATENT

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## *A Method and Apparatus for Transceiving Data Using a Bimodal Power Data Link Transceiver Device*

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**A Method and Apparatus for Transceiving Data Using a  
Bimodal Power Data Link Transceiver Device**

BACKGROUND OF THE INVENTION

1. Field of the Invention

5 The present invention relates to data link transceivers and, more particularly, to data link transceivers requiring low receiver power and high burst transmission power.

2. Prior Art

10 A number of user applications continue to drive the requirement for low power transceivers capable of radio frequency communication. These applications include battery powered smart weapons capable of being remotely armed or disarmed, or simply located. Specific examples may include explosive mines, of the land or sea variety, missiles, or even small arms. The need for low power  
15 transceivers in mines is clear when considering thousands of deployed and armed land mines that are left after a conflict. These armed and very dangerous land mines essentially continue the original conflict against unarmed non-combatants and are of grave public concern as  
20 well as the subject of international treaties. Thus, there exists a need for arming or disarming the mines as necessary. There also exist a need for locating mines for removal or rendering inoperative, i.e., exploding the mine. It has been suggested in the prior art that this  
25 functionality for arming or disarming mines may be accomplished by a transceiver - see U.S. Patent No. 6,014,932.

Yet, a simple transceiver with its high receiver power requirements falls short when considering that devices such as land mines are battery powered - and that the mine may need to be controlled over many months with the landmine in a continuous receive mode. The power availability problem is exacerbated in smaller devices with smaller power cells; nor does providing larger or more power cells solve the problem since many devices such as landmines have weight or size limitations.

In addition, because the many users in a combat environment crowd the electromagnetic spectrum, communications with the landmine must be within a certain predefined range, such as below 200 MHz.

Thus, the above suggests a need for a transceiver having low power requirements. Such a low power transceiver may be fabricated through an Application Specific Integrated Circuit (ASIC). Yet, ASICs, custom made integrated circuits, are expensive and generally cost prohibitive due to their relatively limited production runs. On the other hand, commercial transceivers having low power consumption generally fall into the very high frequency range category, on the order of giga-hertz, well above the 200 MHz operating region of a landmine, and/or fail to have enough power to operate in a burst transmission mode.

#### SUMMARY OF THE INVENTION

In accordance with one embodiment of the invention a bimodal power data link transceiver device is provided.

The device comprises a transceiver integrated circuit (IC), having: a transmitter, a receiver; a second power amplifier coupled to a first power amplifier; a transmit/receive switch coupled to the second power  
5 amplifier and the receiver; a controller coupled to the transceiver IC; a direct digital frequency synthesizer coupled to the transceiver IC; a second voltage controlled oscillator (VCO) coupled to the transmitter; and a loop filter coupled to the second VCO and the  
10 transceiver IC.

In accordance with another embodiment the invention includes a method for transceiving data in a device adapted to transceiving data in the radio frequency spectrum below 200 MHz. The method includes the steps of  
15 providing a transceiver integrated circuit (IC), the transceiver IC and generating a voltage controlled oscillator (VCO) signal for input to an IC oscillator port. The steps include coupling a direct digital synthesizer (DDS) to an IC frequency reference port; and  
20 coupling the radio frequency output port to a power amplifier; and coupling a radio frequency input port to a transmit/receive switch.

Another embodiment of the invention is directed towards an improved bimodal power data link transceiver device.  
25 The device includes a receiver section; a transmitter section; and a phased locked loop (PLL) frequency generator section. The PLL frequency generator section has a voltage-controlled oscillator (VCO), and a direct digital synthesizer (DDS). The PLL frequency generator is  
30 coupled to the receiver section and the transmitter section, while a controller section is coupled to the PLL frequency generator section and the receiver section.

## BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing aspects and other features of the present invention are explained in the following description, taken in connection with the accompanying drawings, wherein:

Figure 1 is a transceiver IC circuit block diagram incorporating features of the present invention shown in figure 3;

Figure 2 is a functional block diagram of the circuit shown in figure 1;

Figure 3 is a pictorial block diagram incorporating features of the present invention;

Figure 4 is a circuit schematic of a typical voltage controlled oscillator (VCO); and

Figure 5 is an exemplary detailed circuit schematic of items 12 and 13, of a voltage controlled oscillator and a loop filter, respectively, incorporating features of the present invention shown in figure 1.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring to Fig. 3, there is shown a block diagram of a system 31 incorporating features of the present invention. The system 31 could be any suitable battery powered system requiring radio frequency transceiving functionality. Exemplary, but not limiting embodiments

include: remote control and/or identification of mines of the land or sea variety, or similarly, remote control and/or identification of small arms weapons. In an alternate embodiment, system 31 could be any suitable type of system with Global Positioning System (GPS) 39 functionality; where the position of the system is transmitted to a remote location, e.g., positional information of sea mines in a mine field or the whereabouts of a particular small arms weapon. It should further be appreciated that these teachings apply as well to commercial and consumer applications.

Thus, although the present invention will be described with reference to the embodiments shown in the drawings, it should be understood that the present invention could be embodied in many alternate forms of embodiments.

As used in this specification, a bimodal power data link transceiver device is defined as a transceiver device having a receiver power mode different from the transmission power mode, i.e., that consumes a different amount of current when receiving than when transmitting.

Still referring to Fig. 3, there is shown one embodiment of a system 31 incorporating features of the present invention. The RF transceiver device 33 is used in battery-powered systems requiring RF transceiving functionality. The battery-powered systems generally comprises a device 32 such as a smart landmine, i.e., the landmine may be armed, detonated, or located remotely, a clock 16 for timing, a data bus connection 18, a bandpass filter 111, and an antenna 112.

The antenna 112 may be any suitable antenna or combination of antennas designed for transmission and/or reception of signals.

5 The bandpass filter 111 may be any suitable type of filter designed to pass the frequencies of interest for transmission or reception. It will be readily appreciated by one skilled in the art that the bandpass filter 111 may have different passbands for reception and transmission.

10 The data bus connection 18 may be any suitable type of physical data bus for transferring data between subsystems such as computer software configuration items (CSCI) and/or hardware configuration items (HWCI). It will be readily appreciated by one skilled in the art  
15 that the data bus may be serial, such as a uniform serial bus (USB) or a parallel data bus. It will also be readily appreciated by one skilled in the art that the data bus 18 may also be a combination of parallel and serial buses. In alternate embodiments the data bus 18 may  
20 include fiber optic lines or light emitting diode (LED) transmitting devices.

Clock 16 may be any suitable type of clock device, such as a crystal oscillator. It will be readily appreciated by one skilled in the art that the clock 16 may be a  
25 dedicated clock, or that the clock may be a master clock for synchronizing subsystems within the device 31. In alternate embodiments the clock 16 may be collocated with the device 31 or, for security purposes, may be physically located elsewhere, and the clock signal is  
30 transmitted to device 31 as required.

Referring also to Fig. 1, there is shown a block diagram of a system 33 incorporating features of the present invention.

Transceiver IC 14 may be any suitable type of transceiver  
5 IC. The transceiver IC 14 could be a Blue Chip  
Communication <sup>TM</sup> BCC 418UHF Transceiver. In general, any  
suitable transceiver with similar characteristics could  
be used. It will be appreciated that a novel feature of  
the present invention allows commercial off the shelf  
10 transceiver ICs to be adapted to applications not  
originally intended by the IC manufacturer. For example,  
the aforementioned 418UHF transceiver is designed to  
operate at a minimum frequency of 300 MHz. However,  
features of the present invention in the preferred  
15 embodiment adapt the IC to operate well below the  
specified frequency and yet maintain the IC's desirable  
operating characteristics. In addition, the remnants of  
the IC's internal VCO may then be utilized for other  
functions not originally anticipated by the manufacturer.  
20 In the preferred embodiment the partial VCO within the  
418 UHF IC is utilized as an emitter follower circuit or  
buffer. In alternate embodiments the remnants may be  
utilized as filters. As noted, features of the present  
invention permit the IC to be used in applications not  
25 originally contemplated by its manufacturer. With general  
regard to transceiver ICs, reference can be had to  
"BCC418 UHF transceiver" reference manual, rev. 1.0, the  
disclosure of which is incorporated by reference in its  
entirety.  
30 In an alternate embodiment any suitable type of low power  
transceiver IC could be provided, such as the one shown  
partially in Fig. 4. It will be appreciated by one



skilled in the art that a suitable transceiver IC will contain a partial VCO and will provide terminals where external frequency-setting components 142A are to be provided to complete the construction of the VCO. An example is shown in Fig. 4.

In the preferred embodiment the components 142A external to the IC 14 are replaced with a complete VCO (Fig. 1, item 12). The VCO 12 may be any suitable type of VCO, for example a Colpitts Oscillator, for use with the transceiver IC 14. In the preferred embodiment the VCO 12 is designed to operate the transceiver IC 14 below 200 MHz. An operational example of the preferred embodiment is shown in Fig. 5.

Still referring to Figs. 1 and 5, loop filter 13 may be any suitable type of filter for optimizing parameters such as FSK modulation rates, PLL lock times, bandwidth, and phase noise. In alternate embodiments dual loop filters may be provided. Loop filters are well known in the art and will not be discussed further. An operational example of the preferred embodiment is shown in Fig. 5

Direct digital frequency synthesizer (DDS) 15 is used to drive buffer 150. In the preferred embodiment buffer 150 is a crystal oscillator capable of being driven by an external frequency source such as the DDS 15. This permits the transceiver to be tuned to the desired channel frequency and/or the transmitter FSK modulation to be set when the DDS 15 controls the reference frequency. In an alternate embodiment DDS 15 may be any suitable frequency agile device or devices capable of introducing various frequency modulations, such as frequency shift keying (FSK) or phase shift keying (PSK).

Clock 16 may be any suitable type of device for providing timing signals. In an alternate embodiment the clock may be remote from the transceiver IC 14. Clocks and timing signals are well known in the art and need not be discussed here.

Controller 17 may be any suitable type of controller capable of controlling transceiver IC 14 and DDS 15.

Transmit/Receive switch 110 may be any suitable RF switch for switching the antenna 112 between transmit and receive sections of the transceiver IC 14. In alternate embodiments the transmit/receive switch could comprise two pin diodes for maximum input sensitivity and output power.

Power amplifier 19 may be any suitable RF power amplifier.

Referring also to Fig. 2 there is shown a functional block diagram of the system shown in Fig. 3.

Transceiver 31 is a radio device for the transmission and reception of digital data. When used with a controller 17 it forms one terminal of a data link. The transceiver may be incorporated into equipment that is easily carried by a person on foot because it is small and lightweight. The preferred embodiment is also low power, on the order of 10 milliamps in the predominant receive mode. There are four main functional blocks, Phase Locked Loop 24, Receiver 21, Transmitter 22, and Micro-controller 23.

A signal enters the transceiver via the antenna 112 and is coupled to the receiver section 21 via the t/r switch 110 and the bandpass filter 111. The receiver block 21

detects incoming RF signals and extracts the digital data contained in the signals. The digital data from the receiver section 21 is sent to an external data processor via bus 18 and micro-controller 23. In the preferred  
5 embodiment the receiver 21 uses a direct conversion architecture in which the incoming signal is amplified by a low noise preamplifier 143 and converted directly to base-band for further amplification, filtering and detection via mixers 145,147 and channel path components  
10 146A-146H. In the preferred embodiment the output of the low noise amplifier 143 is coupled to the quadrature product detector 145,147 for conversion to two base-band signals 90 degrees out-phase. Both baseband signals are amplified and filtered by active low pass filters  
15 146B,146E. The bandwidth of the base-band filters 146B,146E is selectable so that various bandwidth signals may be received without the necessity of modifying the circuit. Filter bandwidths are selectable by the micro-controller 17. The baseband signals are then coupled to  
20 the demodulator 146H. In the preferred embodiment the use of the direct conversion receiver simplifies the receiver because it eliminates the need for tuned amplifiers and filters at an intermediate frequency (IF).

The Received Signal Strength Indicator (RSSI) detects the  
25 presence of a received signal. A signal appearing on the RSSI is used to turn on sections that are normally powered down to conserve power. In an alternate embodiment the RSSI signal could be used to activate a GPS (Fig. 3, item 39) subsystem in order to determine the  
30 device location. In this alternate embodiment the positional latitude and longitude information from the activated GPS could be linked to micro-controller 17;

micro-controller 17 encodes the information through DDS 15, which subsequently modulates the RF carrier emanating from VCO 12. Alternatively, modulation of the carrier wave may also be achieved in this alternate embodiment, or in any of the other embodiments by modulating the VCO 12 directly.

In the transmit mode, the output of the power amplifier 141 is turned on and the transmitter output is frequency shift keying (FSK) modulated by setting the output frequency of the DDS 15 to the appropriate mark and space frequencies. It will be readily appreciated that prior to the turn on of the power amplifier 141 the device is advantageously in a low power receive state. The PLL output frequency from the PLL section 24 to the output amplifier 141 dynamically tracks reference frequency marks and spaces from the DDS 15. Thus, advantageously, the output of PLL section 24 is dynamically tuned to the transmit frequency and in this manner the output RF signal is FSK modulated. In an alternate embodiment other modulation schemes could be employed in a similar fashion. As stated above, modulation of the carrier wave may also be achieved by modulating the VCO 12 directly.

The micro-controller section 23 controls the transceiver 14 and provides a digital interface with data bus 18. The controller 17 programs the frequency of the DDS 15 and the transceiver IC 14.

It should be understood that the foregoing description is only illustrative of the invention. Moreover, it will be readily appreciated that an advantage of the present invention is the use of inexpensive commercial off the shelf (COTS) components for military applications. It

will be further appreciated that advantages of the present invention include low power, low weight, RF transceiver operation. Thus, various alternatives and modifications can be devised by those skilled in the art without departing from the invention. For example, the relatively small size of the invention could permit alternate embodiments with a GPS system being covertly installed in a variety of objects for later relocation. Examples could be locating stolen automobiles, marine vessels, computers, or virtually any object capable of containing the device. Accordingly, the present invention is intended to embrace all such alternatives, modifications and variances that fall within the scope of the appended claims.

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